

Serial No. 09/805,138
Docket No. BUR919980050US4

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AMENDMENTS TO THE CLAIMS:

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1-14. (Canceled).

15. (Previously presented) A method of reducing power in a portable microprocessor,
including:

obtaining an opcode group and obtaining a control signal therefrom;
determining whether said control signal is active;
if said control signal is determined to be active, setting the control signal to active;
running a test case for the opcode group to determine whether the opcode passes; and
if said opcode group passes, marking the control signal.

16. (Currently amended) A method of reducing power in a portable microprocessor,
including:

obtaining an opcode group and obtaining a control signal therefrom;
determining whether said control signal is active;
if said control signal is determined to be active, setting the control signal to active;
running a test case for the opcode group to determine whether the opcode passes;
if said opcode group passes, marking the control signal,

~~The method according to claim 15, further including:~~

determining whether any other control signals exist for the opcode group;
when no more control signal exists, obtaining a next opcode group and testing and
determining whether control signals of said next opcode should be marked; and

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when no more opcode groups exist, setting all marked control signals to active and executing a regression analysis thereon.

17. (Previously presented) The method according to claim 16, further comprising:
- when said regression analysis fails, performing debugging;
 - when said regression analysis passes, setting all marked signals to their previous states and executing another regression analysis thereon;
 - when said another regression analysis fails, performing debugging; and
 - when said another regression analysis passes, performing checking in the unit.

18-21. (Canceled).

22. (Previously presented) A signal-bearing medium tangibly embodying a program of machine-readable instructions executed by an apparatus to perform a method of reducing power in a portable microprocessor, said method including:
- obtaining an opcode group and obtaining a control signal therefrom;
 - determining whether said control signal is active;
 - if said control signal is determined to be active, setting the control signal to active;
 - running a test case for the opcode group to determine whether the opcode passes; and
 - if said opcode group passes, marking the control signal.